

Docket No.: 67161-117

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of | : | Customer Number: 20277 |
| | : | |
| Shinji TANAKA | : | Confirmation Number: |
| | : | |
| Serial No.: | : | Group Art Unit: |
| | : | |
| Filed: October 10, 2003 | : | Examiner: Unknown |
| | : | |
| For: | | SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND PARITY DATA IN BURST OPERATION |

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

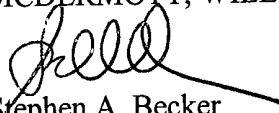
The relevance of Japanese Patent Laying-Open No. 11-65944 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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